Division of Engineering, Computer Programming, & Technology Department of Electronics Engineering Technology West Campus Building 9, Room 140 (407) 582-1902/1903 http://www.valenciacollege.edu/west/engineering/



Spring 2017

Course Syllabus: EET 1015C – Fundamentals of DC Circuits – CRN 13197 (3 Credits hours)

Professor's Information:

Instructor:	Prof. Arif Rafay
Office:	West Campus, Bldg. 11 – Room 253
Phones:	407-443-1023 (Cell)
Email:	arafay@

Office Hours:

Day	Time	Location	
Monday	12:00 noon to		
	12:50pm	Room 11 - 253	
Tuesday	1:00 pm to 5:00	Room 11 - 253	
	pm		
Wednesday	11:00 am to		
	12:50pm	Room 11 - 253	
Thursday	11:00 am to	Room 11 - 253	
	12:50pm		
Friday	8:00 am to 12:00	Virtual(email, phone)	
	noon		

Textbook: <u>Principles of Electric Circuits</u>, Conventional Current Version, Floyd, 9th Ed. ISBN: 9780135073094, Publisher: Pearson.

Lab Manual: *Fundamentals of DC & AC Circuits Laboratory Manual*, Hedayat, Nasser

Prerequisite: MTB 1329C and EET 1214C

Class Time and Location:

Lecture: Monday, 1:00 – 4:30 PM, Bldg. 11 – Room 244

Catalog Course Description: Fundamental course in DC electric circuits. Prepares student for EET 1025C and subsequent advanced courses. Classroom lectures supplemented with laboratory projects to provide student with hands-on experience in use of electronic test equipment and in proper techniques for data measurements/interpretation, trouble-shooting and orderly documentation of test results and conclusions. (Special Fee: \$67.00)

Course Learning Outcomes – This course requires the student to demonstrate the following:

- Understand the basic units, power of ten notations, scientific notation, engineering notation, metric prefixes, and unit conversions
- Understand basic concept of electrical charge, voltage, current, resistance, and Ohm's law
- Understand power, energy, resistors power ratings, voltage drop in a resistance
- Study resistors in series, KVL, Ohm's Law for a series circuit, voltage dividers, power in a series circuit, circuit ground, and troubleshooting series circuits
- Study resistors in parallel, KCL, Ohm's Law for a parallel circuit, circuit dividers, power, and troubleshooting parallel circuits
- Identify and analyze Series-Parallel circuits, Ladder networks, as well as troubleshooting
- Study source transformation, the Superposition theorem, Thevenin & Norton theorems, Maximum Power theorem, and Delta-Wye conversions
- Study the Branch, Mesh, and Node Analysis using the Substitution method and the Determinant Method

DISCLAIMER: Changes in this syllabus may be made at anytime at the instructor's discretion.

Student and Performance Assessment:

Laboratory Experiments ¹	300
Homework	100
Two Exams	400
Final Exam ²	200

 Grade Scales

 A
 90 - 100 %

 B
 80 - < 90 %</td>

 C
 70 - < 80 %</td>

 D
 60 - < 70 %</td>

 F
 < 60 %</td>

¹ <u>No Late work will be accepted</u>.

² Final Exam will be <u>comprehensive</u>

DISCLAIMER: Changes in this syllabus may be made at anytime at the instructor's

discretion.

¹ <u>No Late work will be accepted</u>. <u>Homework is collected at the beginning of</u> <u>class</u>.

² <u>No make-up</u> quizzes or exams will be given.

Important Dates:

Monday, January 16MLK Birthday – College is closed.Wed, Jan 20 – Fri Jan 29 No Show Reporting PeriodMon. – Sun., Mar. 13 – 17 Spring BreakFriday, March 31, 2017Withdrawal deadline for "W" GradeMon. – Sun., Apr. 24 – Apr 28Final Exams WeekTuesday, May 2Final Grades Viewable in ATLAS

Tentative Course Outline for EET 1015C – CRN 13197; Spring, 2017					
Week - Tuesda y	Chapter	Material To Be Covered	Lab Assignment(s)	Due Material	
Week 1 01/09	1 2	Syllabus and Course Overview Quantities and Units Voltage, current, and Resistance			
01/16		MLK Day (No Class)			
Week 3 01/23	2	Voltage, current, and Resistance – Continued	Conduct Experiments 1		
Week 4 01/30	3 4	Ohm's Law Energy and Power	Conduct Experiment 2	Lab Report: Exp. 1	
Week 5 02/06	5	Series Circuits Experiment 3		Lab Report: Exp. 2	
Week 6 02/13		Exam 1(Chapters 1, 2, 3, 4, and 5)	Lab Exam Experiments 1, 2 , 3	Lab Report 3	
Week 7 02/20	6	Parallel Circuits	Conduct Experiment 4		
Week 8 02/27	6	Parallel Circuits – Continued		Lab Report: Exp. 4	
Week 9 03/06	7	Series – Parallel Circuits			
Week 9 03/06	7	Series – Parallel Circuits – Continued	Conduct Experiments 5		
Week 10 03/13		Spring Break		Experiments 5	
Week 11 03/20		Exam 2(Chapters 6, 7) Lab Exam(Experiments 4, 5)			
Week 12	8	Circuit Theorems & Conversions Experiment 6			

03/27		Source Conversion		
Week 13 04/03	8	Circuit Theorems & Conversions –	Experiment 7	Experiment 6
Week 14 04/10	8	Circuit Theorems & Conversions – Cor Experiment 8	tinued	Experiment 7
Week 14 04/10	8	Circuit Theorems & Conversions – <i>Maximum Power Transfer Theorem</i>	Conduct Experiment 9	Lab Report: Exp. 8
Week 15 04/17	9	Branch, Loop, and Node Analyses – Branch & Loop Analysis Experiment 10		Lab Report: Exp. 9
Week 16 04/24		Final Exam		Experiment 10 Experiment 9

Lab (Assignments) Requirements

- A written lab report will accompany every exercise done in this course. All Labs will be due the week following the start of the lab unless decided otherwise by the Professor. It is the student's responsibility that all labs are handed in by the due date.
- Every report should be typed. <u>NO hand written reports will be accepted</u> (hand-drawn tables within the body of the report or scanned materials are not accepted). Microsoft Word or Excel can be used to create tables, diagrams, and graphs.
- All labs must be done <u>during assigned lab</u> time. Labs will only be accepted if performed during the assigned class time <u>unless prior approval by the instructor</u> <u>is granted</u>.
- Pre-Lab (MultiSIM) reports must be prepared and presented before the same day as the lab is being performed. Remember that grades are assigned for this pre-lab report.
- Lab Approval All lab exercises must be approved and <u>signed</u> by the instructor or lab personnel. Labs without signatures will not be accepted.
- Must be ready to perform the required laboratory exercises upon arrival to the lab.

 For Further Lab Report instructions and guidelines visit course Blackboard webpage.

Departmental Rules and Requirements

- **Absolutely no food or drinks** are allowed in the classrooms or laboratories.
- All Assignments are due at the <u>beginning of class period</u>.
- It is highly recommended to visit the <u>EET Open Lab</u> (bldg. 9, Room 211) for assistance and practice.
- **Quizzes & Exams**:
 - > Are given at the *beginning of the class*.
 - Work must be properly and adequately organized and shown to earn credit.
 - No make-up quizzes or exams are permitted unless <u>prior arrangement</u> with instructor has been made and <u>approved</u>.
- □ There are no *dropped* exam scores.
- □ Final exam is required. Failing to take the final exam will result in grade F.
- You are expected to be in class <u>on time</u>. You are responsible for any information and/or assignments given during class, whether you are present or not.
- More than <u>two (2) unexcused</u> absences could result in withdrawal from the course or grade F.
- It is your responsibility to withdraw from the course. Failure to do so may result in grade F.
- You are encouraged to ask relevant questions during class.
- If you wish to discuss your grades please visit my office. Valencia prohibits disclosure of grades over the phone or e-mail except through your Atlas account.
- No audio or video recording allowed in class unless prior permission is granted from professor and every other student in the class.
- Cheating: Using any human, written, electronic, or other resource in any manner not explicitly authorized by the instructor will result in a grade of zero on the exam(s) or assignment(s) involved. Any student caught cheating; the instructor has the right to withdraw the student from the class and recommend expulsion from the program.
- Disruptive Behavior: Any student engaging in disruptive behavior will be advised on the first offense and will be <u>dropped</u> from the course on the second offense.

Students are strongly encouraged to read the Valencia policy Manual <u>Student Code of</u> <u>Conduct</u> and <u>Computer Acceptable Usage</u> and <u>Student Core Competencies</u> found at the following links:

http://valenciacollege.edu/generalcounsel/policy/ValenciaCollegePolicy.cfm?policy ID=180,

http://valenciacollege.edu/competencies

<u>Students with disabilities</u> who qualify for academic accommodations must provide a letter from the Office for Students with Disabilities (OSD) and discuss specific needs with the professor, preferably during the first two weeks of class. The Office for Students with Disabilities determines accommodations based on appropriate documentation of disabilities (West Campus SSB 102, ext. 1523).

DISCLAIMER: Changes in this syllabus may be made at anytime at the